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Pat. No. 7,164,206**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Grant Kloster et al. Docket: P11026
Patent No.: 7,164,206 Issued: 01-16-2007
Title: STRUCTURE IN A MICROELECTRONIC DEVICE INCLUDING A BI-
LAYER FOR A DIFFUSION BARRIER AND AN ETCH-STOP LAYER

REVOCATION AND POWER OF ATTORNEY
CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please
revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent
agents to prosecute this application and to transact all business in the Patent and Trademark
Office in connection therewith:

Customer Number: 59796**CERTIFICATE UNDER 37 CFR § 3.73(b)**

Intel Corporation hereby certifies that it is the assignee of the entire right, title and interest
in the patent application identified above by virtue of an assignment from the inventor recorded
on July 14, 2010 Reel 024681, and Frame 0295. To the best of my knowledge and belief, title is
in Intel Corporation, the assignee.

Pursuant to 37 C.F.R. § 3.73(b) I hereby declare that I, Pam J. Matlock, am empowered to
sign this certificate on behalf of Intel Corporation, the assignee.

I hereby declare that all statement made herein of my own knowledge are true, and that
all statements made on information and belief are believed to be true.

Please direct all correspondence in this case to:

Rahul Engineer
c/o CPA Global
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Date 8-18-10

By Pam Matlock
Pam J. Matlock
Secretary of Patents,
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